Investigation into Power Quality Issue due to House Hold Equipments

Keerthi K¹, Sri Kiran Kumar VG², Sri Pramod Antony D’Sa³

¹MTech Student, Dept of PG Studies, VTU Extension Centre, SCEM, Mangaluru
²Associate Professor, Dept of E&CE SCEM, Mangaluru
³Assistant Professor, Dept of E&EE, MIT, Manipal
¹ keerthikundapur1@gmail.com, ² kiran.ec@sahyadri.edu.in, ³ pramod.dsa@manipal.edu

Abstract: With the ever increasing demand for electricity, conservation of energy is given importance. Conventional method of control is being replaced with electronic methods of control. With this harmonics are introduced into the power system. This is applicable to all types of electrical loads, industrial, domestic and commercial. As on now, the presence of harmonics in domestic sector is not monitored, as the quantity of electricity used by domestic sector is not significant as compared to industrial and commercial sector. Yet with the lighting sector dominated by CFL bulbs and LED bulbs, fans being controlled electronically and increasing use of desktops, laptops, laser printers, air conditioners etc., it is a matter of concern to study the harmonics produced by these devices and its effect on distribution transformer and neighbouring utility services. Harmonic analysis is done using expensive power analysers, digital storage oscilloscope etc., In this project a simple method of analysing harmonics is discussed. Initially the voltage and current drawn is sensed, fed to the computer using data acquisition card, total harmonics, 3rd, 5th, 7th and 9th harmonics, power factor etc., is measured. The average values of these is taken and represented as source in Matlab Simulink, passive filters are designed and analysed using simulation.

Keywords-- Power Quality, harmonics analysis, domestic load, passive filter, THD
I INTRODUCTION

Electric power quality has become an important part of power systems. Power quality is the term used to assess and to maintain good quality of power at the point of generation, transmission, distribution and utilization of AC power. Some of the major causes of disturbances in power systems are presence of harmonics, voltage fluctuations, voltage dips, power frequency variations, transients and so on. Harmonics are of major concern mainly due to increase in nonlinear loads on the distribution system with the effect of deterioration power quality significantly.

II PREVIOUS WORK

Determination of power quality is of prime importance today, with the increasing trend of using non-linear devices [1]-[3]. Harmonics produces various deteriorating effects in generators, transformers, circuit breakers and fuses, increase in neutral current etc., [2][5].

IEEE has provided suitable guidelines for control of harmonics [2]. Even though the power consumed by domestic load is not significant, with the increasing trend of using nonlinear devices like CFLs, LED bulbs, electronic fan regulators, laptops and laser printers the distortion on power system will be increasing in great proportion. The distortions can be observed individually by comparing different harmonic components. More general approach to quantify the distortions is the use of total harmonic distortion level (THD) [4]. The different methods of harmonic analysis can be classified under the time domain and frequency domain [6].

Reference [7] discusses the modelling of nonlinear electrical loads used in domestic and small scale industrial distribution systems based on practical waveforms of voltage and current obtained in the laboratory using PSCAD/EMTDC package.

Another research study [8] makes an effort to evaluate the harmonic distortion in the distribution systems caused by the various non-linear residential loads using Electrical Transient Analyzer Program (ETAP).

In another study [9], the impacts of various home nonlinear electric equipment on the power quality of electrical distribution system is studied. The influence of operating modes on the harmonic pollution is studied using the CA 8334B three-phase power quality analyzer. It is found that although home electric appliances are low power receivers, the cumulative effect produced by a large number of small harmonic sources can be substantial.

National Instrument’s white paper [10] describes the use of its software Lab VIEW as powerful monitoring and analysis tool. It states that power disturbances arising within customer facilities have increased significantly due to the increasing use of energy efficient equipment. The monitoring and data collection of power disturbances therefore has to be conducted at the users’ premises. A configurable and programmable PXI/Compact PCI, high-performance PC with high-speed data acquisition units and VI software using LabVIEW can perform the required task.

Virtual instrumentation utilizes simulation of power system monitoring using LabVIEW, a graphical programming language. By using the powerful tools of LabVIEW [11] various harmonics are simulated within the G programming/coding.
In reference [12] Matlab Simulink has been used to model FL and CFL for harmonic studies. Since Matlab simulation offers versatility for harmonic analysis, the same has been in this project for design of passive filter and analysis.

### III Methodology

#### 3.1 Circuit Diagram

The circuit diagram is shown in Fig (1). Voltage and current signal from the supply system is sensed using Hall Effect sensors. The standard instrument transformers cannot be used for this purpose as they are designed and calibrated for 50Hz. But some of the power quality disturbances of interest like harmonics, transients require the measurement of significantly higher frequencies. For those frequencies the accuracy of the instrument transformers can be no longer taken for granted. Hall Effect sensors suited the application in all the ways.

![Circuit Diagram](image)

**Fig (1) Circuit for measuring harmonics**

The data acquisition hardware NI cDAQ-9184, which can measure up to 128 channels of sensor, analog I/O and digital I/O signals is used. Standard 802.3ab Gigabit Ethernet Communication Interface is used. Using NI signal streaming technology, it delivers high speed data. NI compact DAQ can integrate the measurement of voltage, current, temperature etc., into a single device that outputs all the data via the same bus interface. Programming becomes easier because we use the same driver for all measurements.

**Harmonic Analyzer** is simulated using **LabVIEW**. **LabVIEW** - Laboratory Virtual Instrument Engineering Workbench is a system-design platform and development environment for a visual programming language from National Instruments. LabVIEW is commonly used for data acquisition, instrument control, and industrial automation on a variety of platforms including Microsoft Windows, various versions of UNIX, Linux, and OS X.
3.2 Measurement of harmonics

Fig (2) Front panel of Harmonic Analyzer

The programming language used in LabVIEW, also referred to as G, is a dataflow programming language. Execution is determined by the structure of a graphical block diagram on which the programmer connects different function-nodes by drawing wires. These wires propagate variables and any node can execute as soon as all its input data become available. Since this might be the case for multiple nodes simultaneously, G is inherently capable of parallel execution. The circuit developed for harmonic analysis is shown in Fig (2).

3.3 Loads Considered: The following domestic loads were considered for analysis.

<table>
<thead>
<tr>
<th>Sl.No.</th>
<th>Domestic Load</th>
<th>Rating/Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Incandescent lamp</td>
<td>100W</td>
</tr>
<tr>
<td>2</td>
<td>CFL lamp</td>
<td>Phillips 8W</td>
</tr>
<tr>
<td>3</td>
<td>CFL lamp</td>
<td>Surya 15W</td>
</tr>
<tr>
<td>4</td>
<td>LED bulb</td>
<td>Crown 7W</td>
</tr>
<tr>
<td>5</td>
<td>LED bulb</td>
<td>Crown 12W</td>
</tr>
<tr>
<td>6</td>
<td>LED bulb</td>
<td>Ever ready 3W</td>
</tr>
<tr>
<td>7</td>
<td>LED bulb</td>
<td>Ever ready 7W</td>
</tr>
<tr>
<td>8</td>
<td>LED bulb</td>
<td>Ever ready 15W</td>
</tr>
</tbody>
</table>
Incandescent load is taken for reference as we know that the harmonics induced by incandescent lamp is negligible. The electronic ballast of CFL lamps cause harmonics. CFL lamps of two brands are considered. LED lamps is replacing all other lamps in lighting sector since power conserved is very high. LED lamps of different brands and ratings are considered for comparison. Desktops are being replaced by laptops. So a laptop charger which causes harmonics is considered. Conventional fan regulators is being replaced by electronic regulators. The performance of this regulator at five different speeds are considered.

Each of these devices are operated separately for 3 minutes and the reading are recorded continuously. The average of these readings are taken and recorded as shown in table (2).

**Table (1) Domestic loads considered**

<table>
<thead>
<tr>
<th>Sl.No.</th>
<th>Device</th>
<th>Voltage</th>
<th>Current mA(RMS)</th>
<th>THD</th>
<th>IHD 1</th>
<th>IHD 3</th>
<th>IHD 5</th>
<th>IHD 7</th>
<th>IHD 9</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>100W Incandescent</td>
<td>236.925</td>
<td>451.783</td>
<td>1%</td>
<td>100%</td>
<td>1%</td>
<td>0%</td>
<td>0%</td>
<td>0%</td>
</tr>
<tr>
<td>2</td>
<td>8W CFL Phillips</td>
<td>238.057</td>
<td>43.333</td>
<td>25%</td>
<td>100%</td>
<td>18%</td>
<td>4%</td>
<td>15%</td>
<td>5%</td>
</tr>
<tr>
<td>3</td>
<td>15 W CFL SURYA</td>
<td>236.168</td>
<td>60.998</td>
<td>27%</td>
<td>100%</td>
<td>19%</td>
<td>3%</td>
<td>16%</td>
<td>5%</td>
</tr>
<tr>
<td>4</td>
<td>7 W LED CROWN</td>
<td>236.869</td>
<td>107.850</td>
<td>24%</td>
<td>100%</td>
<td>15%</td>
<td>11%</td>
<td>8%</td>
<td>7%</td>
</tr>
<tr>
<td>5</td>
<td>12W LED CROWN</td>
<td>237.386</td>
<td>110.077</td>
<td>25%</td>
<td>100%</td>
<td>17%</td>
<td>13%</td>
<td>9%</td>
<td>5%</td>
</tr>
<tr>
<td>6</td>
<td>3W LED Everready</td>
<td>237.291</td>
<td>20.774</td>
<td>17%</td>
<td>100%</td>
<td>15%</td>
<td>7%</td>
<td>4%</td>
<td>3%</td>
</tr>
<tr>
<td>7</td>
<td>7W LED EVERREADY</td>
<td>237.103</td>
<td>39.497</td>
<td>13%</td>
<td>100%</td>
<td>10%</td>
<td>6%</td>
<td>5%</td>
<td>4%</td>
</tr>
<tr>
<td>8</td>
<td>15W LED Everready</td>
<td>237.992</td>
<td>77.238</td>
<td>26%</td>
<td>100%</td>
<td>21%</td>
<td>7%</td>
<td>10%</td>
<td>4%</td>
</tr>
<tr>
<td>9</td>
<td>5W LED Scholar</td>
<td>233.188</td>
<td>74.680</td>
<td>19%</td>
<td>100%</td>
<td>11%</td>
<td>8%</td>
<td>9%</td>
<td>8%</td>
</tr>
<tr>
<td>10</td>
<td>Laptop Charger</td>
<td>237.152</td>
<td>294.776</td>
<td>165%</td>
<td>100%</td>
<td>90%</td>
<td>83%</td>
<td>73%</td>
<td>63%</td>
</tr>
<tr>
<td>11</td>
<td>Fan low Speed</td>
<td>104.559</td>
<td>260.138</td>
<td>94%</td>
<td>100%</td>
<td>76%</td>
<td>43%</td>
<td>23%</td>
<td>20%</td>
</tr>
<tr>
<td>12</td>
<td>Fan second speed</td>
<td>118.207</td>
<td>277.768</td>
<td>86%</td>
<td>100%</td>
<td>72%</td>
<td>36%</td>
<td>20%</td>
<td>19%</td>
</tr>
<tr>
<td>13</td>
<td>Fan third speed</td>
<td>137.554</td>
<td>301.558</td>
<td>74%</td>
<td>100%</td>
<td>64%</td>
<td>26%</td>
<td>19%</td>
<td>15%</td>
</tr>
<tr>
<td>14</td>
<td>Fan fourth speed</td>
<td>181.927</td>
<td>351.314</td>
<td>50%</td>
<td>100%</td>
<td>43%</td>
<td>16%</td>
<td>14%</td>
<td>10%</td>
</tr>
<tr>
<td>15</td>
<td>Fan High Speed</td>
<td>224.050</td>
<td>393.846</td>
<td>15%</td>
<td>100%</td>
<td>9%</td>
<td>8%</td>
<td>6%</td>
<td>4%</td>
</tr>
</tbody>
</table>

**Consolidated table showing the harmonics contents of the domestic appliances considered**
3.4 Design of Passive shunt Filter

For any selected harmonic, eg 3rd harmonic, the series combination of inductor and capacitor is tuned and this acts as a low impedance path for that harmonic(3rd in this case). The size of the capacitor is calculated from the reactive power requirement ($Q_c$) of the load.

The absolute value of capacitance ($C_n$) is calculated as

$$ C_n = \frac{Q_c}{m \omega V_s^2} $$  \hspace{1cm} (1)

Where $m$ is the number of branches in the passive filter for all the phases, $V_s$ is per-phase fundamental voltage across it and $\omega$ is the fundamental frequency of the supply.

The inductance of the $n^{th}$ order filter is calculated as

$$ L_n = \frac{1}{n^2 \omega^2 C_n} $$  \hspace{1cm} (2)

The series resistance for the inductor of the $n^{th}$ order filter is calculated as

$$ R_{n} = \frac{n \omega L_n}{Q_n} $$  \hspace{1cm} (3)

Where $Q_n$ is the quality factor of the inductor of the $n^{th}$ order filter, which is normally considered as $10 < Q < 100$.

3.5 Modelling, Simulation and Performance of Passive Power Filter

In order to check the effectiveness and presence of resonance, the filters are connected in the system configuration and waveform analysis is done through simulation using Matlab Simulink. Matlab Simulink is the most versatile software for FFT analyzing using Powergui feature.

Based on the result obtained from Lab-view, average values of 3rd, 5th, 7th and 9th harmonic content is considered. The percentage content of higher harmonics were found to the negligible. Hence those harmonics are not considered. Harmonic current sources are created in Simulink based on the average values. The combined effect of this is considered for designing the filter. Separate filter branches are designed for each harmonic. Current waveforms are analysed before and after the insertion of the filter. Total FFT is analysed using Powergui feature of Simulink.
3.6 Generation of harmonic source

Based on the average values of harmonics generated, harmonic source is created using Matlab Simulink as shown in Fig (3). This harmonic source is considered as source for measurement of harmonics and its reduction using passive filters as shown in Fig (4).

IV RESULTS

Laptop Charger

4.1 Current Waveform


4.2 FFT

Looking into the consolidated table, it is seen that harmonics in case of laptop charger is highest with a total harmonic distortion of 165%. Hence a filter is designed for laptop charger.

Apparent Power = \( V_s \times I = 237.152 \times 294.776 \times 10^{-3} \)

\[ = 69.906 \text{ volt-amperes.} \]

Real Power \( = 60.327 \text{ Watts} \)

Reactive Power = \( \sqrt{(\text{Apparent power} - \text{Real power})^2} \)

\[ = \sqrt{(69.906^2 - 60.327^2)} \]

\[ = 35.319 \text{ VAR reactive} \]

\[ C_n = \frac{Q_c}{mV^2} = \frac{35.319}{2 \times 2 \times 3.14 \times 50 \times 237.152^2} = 1 \times 10^{-6} \text{ Farad} \]
For 3rd harmonic

\[ L_n = \frac{1}{n^2 \omega^2 C_n} = \frac{1}{3^2 \times 314^2 \times 1 \times 10^{-6}} = 1.126 \text{ henry} \]

\[ R_{\text{ns}} = \frac{n \omega L_n}{Q_n} = \frac{3 \times 314 \times 1.126}{100} = 10.6 \text{ ohm} \]

For 5th harmonic

\[ L_n = \frac{1}{n^2 \omega^2 C_n} = \frac{1}{5^2 \times 314^2 \times 1 \times 10^{-6}} = 0.40536 \text{ henry} \]

\[ R_{\text{ns}} = \frac{n \omega L_n}{Q_n} = \frac{5 \times 314 \times 0.40536}{100} = 6.364 \text{ ohm} \]

For 7th harmonic

\[ L_n = \frac{1}{n^2 \omega^2 C_n} = \frac{1}{7^2 \times 314^2 \times 1 \times 10^{-6}} = 0.20681 \text{ henry} \]

\[ R_{\text{ns}} = \frac{n \omega L_n}{Q_n} = \frac{7 \times 314 \times 0.20681}{100} = 4.545 \text{ ohm} \]

For 9th harmonic

\[ L_n = \frac{1}{n^2 \omega^2 C_n} = \frac{1}{9^2 \times 314^2 \times 1 \times 10^{-6}} = 0.125 \text{ henry} \]

\[ R_{\text{ns}} = \frac{n \omega L_n}{Q_n} = \frac{9 \times 314 \times 0.125}{100} = 3.5325 \text{ ohm} \]

Filters are designed using the above values.

\section*{V ANALYSIS}

Laptop charger was considered as the THD of it is higher compared to all other devices. Individual harmonic distortion and total harmonic distortion is measured at various points of the circuit, like before the insertion of the filter and after the insertion of each of the above designed filter, using Powergui feature of Matlab Simulink.

\subsection*{5.1 Source current along with harmonics- FFT Analysis}

Total harmonics distortion is found to be 152.66%
5.2 Output after removal of 3\textsuperscript{rd} harmonic

Total harmonic distortion is reduced to 121%

5.3 Output after removal of 3\textsuperscript{rd} and 5\textsuperscript{th} harmonic

Total harmonic distortion is reduced to 92%

5.4 Output after removal of 3\textsuperscript{rd}, 5\textsuperscript{th} and 7\textsuperscript{th} harmonic

Total harmonic distortion reduced to 60%

5.5 Output after removal of 3\textsuperscript{rd}, 5\textsuperscript{th}, 7\textsuperscript{th} and 9\textsuperscript{th} harmonic

Total harmonic content reduced to 25%
It is observed that the THD has been reduced from 152% to 25%. Hence the overall power quality is improved.

VI Future scope of the work

The harmonics in CFL, LED, Fan regulator and laptop charger is measured individually. The same can be extended to include air conditioner, microwave oven, induction heater, refrigerator etc., Also these devices can be connected in different parallel combination and the total harmonics can be analysed.

Passive filter have been designed here for simplicity. Effects of using active filters can be looked into.

REFERENCES


